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Chernihiv Polytechnic National University



Erasmus+ Project 101127683 — DIGITRANS

Digital transformation of HEIs education process in Ukraine and Moldova for sustainable engagement with enterprises

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<https://stu.cn.ua/en/>

SMSE & SREE workshop, Online
December 9, 2024

SMSE & SREE seminar

Purpose - preparation for the use of DLE in the project

tasks

- 1. Sharing experience in creating SMSE courses**
- 2. Discussion of existing problems in SMSE using**
- 3. SMSE courses aspects of WP4**
- 4. SREE pilot presentation**

WP3: Status of SREE development

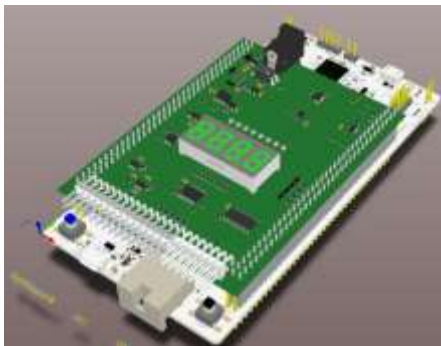
N	Activity	Status	Term	Responsible
1	Scope&Features	Done	March, 2024	Kazymyr
2	Plan of procurement registration	Done	June, 2024	Kazymyr
3	SREE development plan upgrading	Done	August, 2024	Kazymyr
4	Tender on equipment purchasing (part 1) launching	Done	September, 2024	Susa
5	Design of Demo boards	Done	September, 2024	Khomenko
	Design of back-end interface	Done	November, 2024	Bayda
6	Design of front end interface	Done/in progress	December, 2024	Veligorsky
7	Tender on equipment purchasing (part 2) launching	In progress	October, 2024	Susa
8	Equipment purchasing (part 1)	Done	October , 2024	Veligorsky
9	Equipment purchasing (part 12	In progress	November, 2024	Susa
10	Equipment installation	Done/In progress	November, 2024	Khomenko, Susa
11	SW installation and testing	In progress	December, 2024	Bayda, Veligorsky
12	SREE and SMSE integration	In planning	February, 2024	Drozd, Gorval

WP3: D3.2 SREE platform - Demonstrator, pilot, prototype

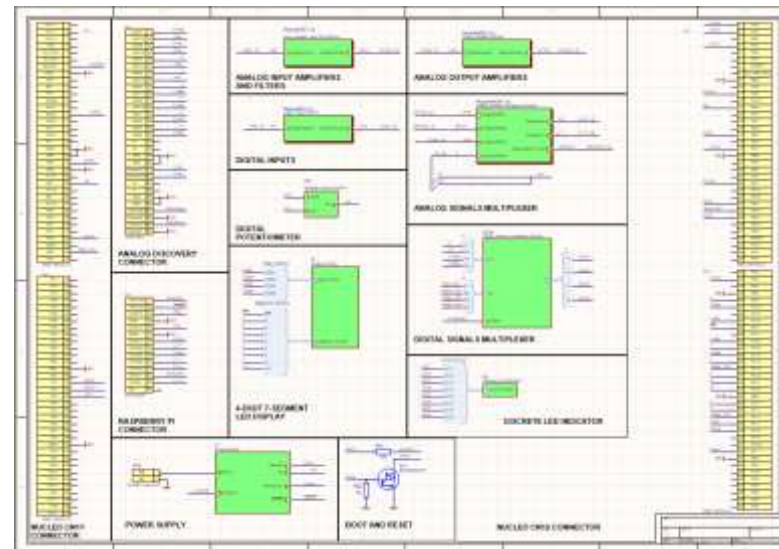
SREE developed equipment and schemes



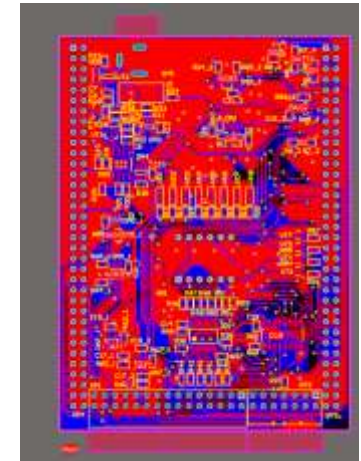
3D model of lab board



3D model of lab board with MCU board



MCU scheme



PCB top and bottom layout

SREE demo and repositories

Link to demo- <http://195.69.76.135:8082/>

Front-end repository:

<https://github.com/BogdanVeligorskyi/mcu-and-fpga-remote-lab-frontend>

Back-end repository:

<https://github.com/vtinkerer/mcu-and-fpga-remote-lab>

FPGA firmware:

<https://github.com/vtinkerer/Raspberry-Pi-Flash-FPGA-using-GPIO>



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